

Research Article

Adaptive Thresholding based Image Binarization Using VHDL

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Abstract: The aim of this study is to recognize the given image with the existing image based on the technique of image binarization by MATLAB tool and it is simulated using VHDL (Very High Speed IC Hardware Descriptive Language) using MODELSIM tool. This image binarization is based on LEGION (Locally Excitatory Globally inhibitory Oscillatory Network) Concept. In this study the algorithm finds an optimum threshold technique, the other by separating the image background and foreground pixels. This algorithm has superior performance in separating the images from background in comparison with the other threshold techniques.

Keywords: Adaptive threshold, binarisation, MATLAB, modelsim, VHDL

INTRODUCTION

To extract the required image from a given image, it may be separated into different components say foreground and background. An adaptive threshold concerning extraction of image from the background in a given image sequences is possible for implementation in hardware. The conventional histogram based threshold methods are deficient in detecting images due to poor contrasts between image and the background, or the change of illuminations (Otsu, 1979; Rosenfeld and De La Torea, 1983; Sezan, 1985; Trier and Taxt, 1995). But in real time applications, except adaptive threshold techniques are not fast enough for hardware implementation (Kittler and Illingworth, 1986). The image binarization (Lee *et al.*, 1991; White and Rohrer, 1983) is a process of dividing the original image into two components, foreground or image and background. The gray level value of background (value '0') is different from the foreground (value '255') values. So, for the effective separation of foreground and background, this thresholding techniques may be considered (Hertz and Schafer, 1988; Seznig and Sankar, 2004). The thresholding techniques yields a binary image as an output by assigning pixels with values less than the threshold as 0's and remaining pixels as 1's. In this study the Image binarization is carried out by MATLAB tool and it is simulated using VHDL (Very High Speed IC Hardware Descriptive Language) using MODELSIM tool (XESS Corporation, 2001). This image binarization is based on LEGION (Locally Excitatory Globally inhibitory Oscillatory Network) Concept (Ligon *et al.*, 1998).

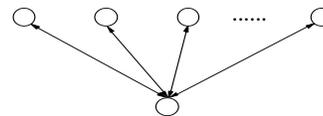


Fig. 1: Comparator model

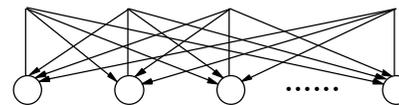


Fig. 2: Globally connected



Fig. 3: Locally connected

The LEGION concept has the following features:

- Binarization is realized in a very short time due to the simple processing.
- Due to the simple structure of each cell representing a pixel compact integration of many cells on a single chip becomes possible.
- Fast software implementation is possible.

Neural oscillator concept: There are three possible ways to reach synchrony as shown in Fig. 1 to 3.

LEGION is composed of a Basic Oscillator, Local Excitatory Connections (produce phase synchrony within each object) and Global Inhibitor (it receives inputs from the entire network and feeds back with

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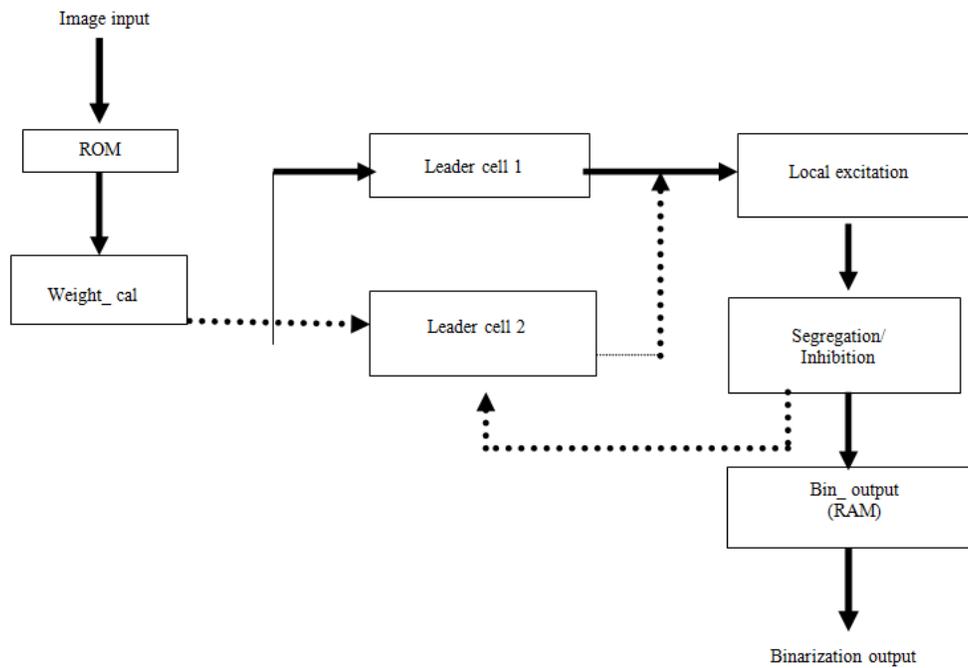


Fig. 4: Block diagram

inhibition). Each Oscillator will respond to a detected feature at some location in the image. Synchronized group of oscillators is separated from other synchronized groups that represent different objects by de-synchronization. It is proposed in Digital implementation, Model described by digital algorithm, both gray/color scale binarization and segmentation in a single algorithm (Yanowitz and Bruckstein, 1989). Re-programmability is possible using FPGA (Shirazi *et al.*, 1995).

The Fig. 4 shows Digital LEGION Image Binarization Architecture. It consists of six blocks namely:

- Input ROM
- Weight calculation
- Leader cell
- Locally excitatory
- Segregation/Inhibition
- Output RAM

DIGITAL LEGION ALGORITHM

Initialization:

Step 1: Initial global inhibitor, $Z = 0$

Step 2: Calculation of Weights

Gray scale images:

$$W_{ij} = \frac{I \max}{1 + |I_i - I_j|}$$

where,

W_{ij} = Weight value between i^{th} and j^{th} pixels

I_i = i^{th} pixel value

I_j = j^{th} pixel value

I_{\max} = Max. Gray level value $[2^n - 1]$

Color image:

$$W(R)_{ij} = \frac{I(R) \max}{1 + |(IR) I - (IR)_j|}$$

$$W(G)_{ij} = \frac{I(G) \max}{1 + |(IG) I - (IG)_j|}$$

$$W(B)_{ij} = \frac{I(B) \max}{1 + |(IB) I - (IB)_j|}$$

$$W_{ij} = \min \{W(R)_{ij}, W(G)_{ij}, W(B)_{ij}\}$$

Step 3: Detection of Leader Cell (P)

$$\sum_{j=1}^N \text{If } (\hat{a} W_{ij} > T_i) \text{ then } P_i = 1, \text{ otherwise } P_i = 0$$

where,

W_{ij} = weight value between i^{th} pixel and j neighboring pixels

$j = 1, 2, 3 \dots N$

T_i = Leader cell threshold value

Step 4: Set all cell to non-excitation $X_i(0) = 0$.

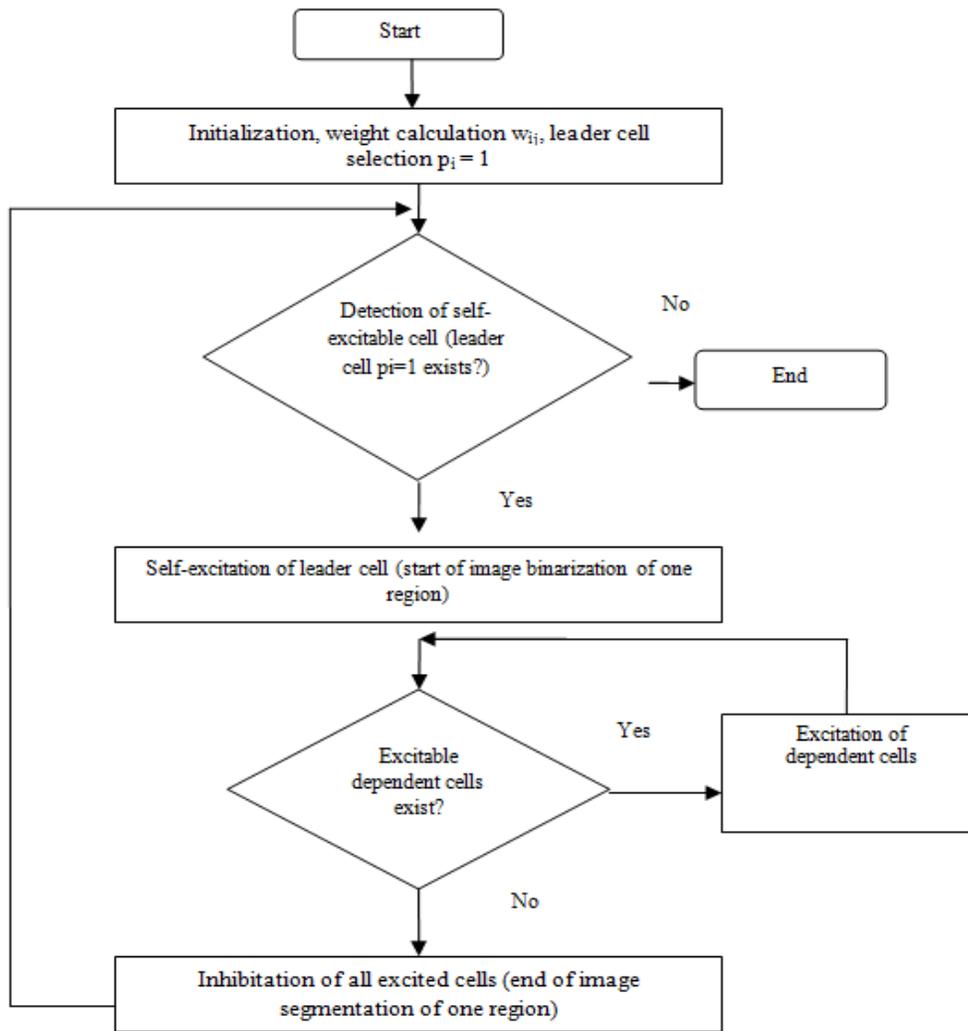


Fig. 5: Flow chart-LEGION algorithm

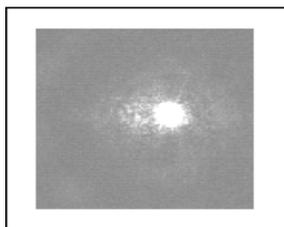


Fig. 6: Sample output

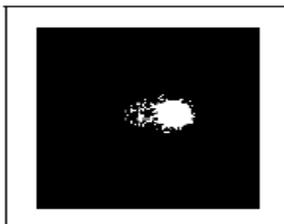


Fig. 7: Binarized image

Self-excitation of leader cell: If (excitable cells = false) then stop; //terminate Otherwise (find leader $P_i = 1$) then

$X_i = 1, Z_i = 1$; goto (step 3) //Self-Excitation Otherwise (step2);

Identification of dependent cell (or) local excitation: If pixel I_i is a neighbor of $X_i = 1$ and $W_i P_i > T_2$ then I_i is a dependent cell of X_i .

Where, $W_i P_i$ = weight between pixel I_i and weight corresponding to $X_i = 1$ location.

Segregation:

- Identify first excitation pixel $X_i = 1$ and read corresponding pixel value I_i
- Identify $X_{i+1} = 1$ pixel, read corresponding pixel value (I_{i+1})
- If $I_{i+1} - I_i < T_3$ then $X_{i+1} = 1$

The weight value between 36-24 is 19, 127-36 is 2, 127-24 is 2 and 127-128 is 127. Let the leader threshold value be 1000.

The corresponding simulated output using VHDL is shown in Fig. 8.

CONCLUSION

In the present study, the image binarization is done by MATLAB and the same is simulated using VHDL by using MODELSIM. In this study the algorithm finds an optimum threshold technique, the other by separating the image background and foreground pixels. This algorithm has superior performance in separating the images from background in comparison with the other threshold techniques.

REFERENCES

- Hertz, L. and R.W. Schafer, 1988. Multilevel thresholding using edge matching. *Comp. Vision Graphic. Image Process.*, 44: 279-295.
- Kittler, J. and J. Illingworth, 1986. Minimum error thresholding. *Pattern Recogn.*, 19: 41-47.
- Lee, S.W., L. Lam and C.Y. Suen, 1991. Performance evaluation of skeletonizing algorithms for document image processing. *Proceeding of the 1st International Conference Document Analysis and Recognition*, pp: 260-271.
- Ligon, W.B., S. McMillan, G. Momm, K. Schoonover, F. Stivers and K.D. Underwood, 1998. A Re-evaluation of practicality of floating point operations on FPGAs. *Proceeding of the IEEE Symposium on FPGAs Custom Computing Machines*, pp: 206-215.
- Otsu, N., 1979. A threshold selection method from gray level histograms. *IEEE T. Syst., Man Cyb.*, 9: 62-66.
- Rosenfeld, A. and P. De La Toree, 1983. Histogram concavity analysis as an aid in threshold selection. *IEEE T. Syst. Man Cyb.*, 13: 231-235.
- Sezan, M.I., 1985. A peak detection algorithm and its application to histogram-based image data reduction. *Graph. Mod. Image Process.*, 29: 47-59.
- Sezgin, M. and B. Sankar, 2004. Survey over image thresholding techniques and quantitative performance evaluation. *J. Electron. Imaging*, 13(1): 146-168.
- Shirazi, N., A. Walters and P. Athanas, 1995. Quantitative analysis of floating-point arithmetic on FPGA based custom computing machines. *Proceeding of the IEEE Symposium on FPGAs for Custom Computing Machines*. California, April 1995.
- Trier, O.D. and T. Taxt, 1995. Evaluation of binarization methods for document images. *IEEE T. Pattern Anal.*, 17(3): 312-315.
- White, J.M. and G.D. Rohrer, 1983. Image thresholding for optical character recognition and other applications requiring character image extraction. *IBM J. Res. Dev.*, 27(4): 400-411.
- XESS Corporation, 2001. Using Xilinx webPack Software to Create FPGA Designs for the XSA Board. Retrieved from: www.xess.com.
- Yanowitz, S.D. and A.M. Bruckstein, 1989. A new method for image segmentation. *Comput. Vision Graph.*, 46: 82-95.